



PATENTS

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Applicant(s): Victor Chan, et al.

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For: SUBSTRATE ENGINEERING FOR
OPTIMUM CMOS DEVICE PERFORMANCE

Dated: July 30, 2003

Commissioner for Patents
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Alexandria, VA 23313-1450

PRELIMINARY AMENDMENT

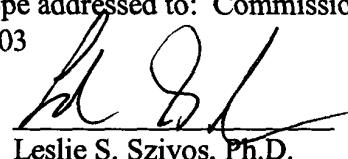
Sir:

In connection with the above-identified patent application, please enter the following amendments.

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents Alexandria, VA 23313-1450 on July 30, 2003

Dated: July 30, 2003



Leslie S. Szivos, Ph.D.

AMENDMENTS TO THE DRAWINGS

The attached sheets, labeled as replacement sheets, include changes to FIGS. 2A-2C, FIG. 4, and FIGS. 5D-5F. The attached sheets replace original sheets 1/5, 2/5 and 4/5 and 5/5.